

CENTRAL ELECTRONICS LIMITED
MATERIALS MANAGEMENT DIVISION

Date: 28-06-2018

CORRIGENDUM-I

Ref: Tender notice no C-2(b)/EOI/7500/242/2018 dated 12-06-2018

Sub: **STRATEGIC ALLIANCE/ TIE UP/JV FOR SETTING UP MANUFACTURING FACILITY FOR CRYSTALLINE SILICON (C-Si) MODULES and/or SOLAR PV CELLS**

In our tender notice/document, please amend the following:

- The last date for bid submission may be read as 25-06-2018, 15:00 Hours IST, instead of 10-07-2018, 15:00 Hours IST.
- The bid opening date may be read as 25-06-2018, 15:30 Hours IST, instead of 10-07-2018, 15:30 Hours IST.

All other terms and conditions will remain unchanged.

For CENTRAL ELECTRONICS LIMITED
sd/-
GENERAL MANAGER
MATERIALS MANAGEMENT DIVISION